

Ref #	Hits	Search Query	DBs	Default Operator	Plurals	Time Stamp
L1	20	("PLL" or "DLL" or (phase or delay) adj2 loop) and integer adj (divisor or divid\$3) and frequency adj multipl\$4 and sampl\$3 adj (means device circuit)	US-PGPUB; USPAT; USOCR; EPO; JPO; DERWENT	OR	OFF	2005/09/28 11:02
L2	8	("PLL" or "DLL" or (phase or delay) adj2 loop) and (controllable adjustable programmable selectable) adj (divisor or divid\$3) and frequency adj multipl\$4 and sampl\$3 adj (means device circuit)	US-PGPUB; USPAT; USOCR; EPO; JPO; DERWENT	OR	OFF	2005/09/28 11:03
L3	13	("PLL" or "DLL" or (phase or delay) adj2 loop) and (controllable adjustable programmable selectable) adj (divisor or divid\$3 or counter) and frequency adj multipl\$4 and sampl\$3 adj (means device circuit)	US-PGPUB; USPAT; USOCR; EPO; JPO; DERWENT	OR	OFF	2005/09/28 11:08
L4	13	3 not 1	US-PGPUB; USPAT; USOCR; EPO; JPO; DERWENT	OR	OFF	2005/09/28 11:04
L5	15	(controllable adjustable programmable selectable) adj (divisor or divid\$3 or counter) and frequency adj multipl\$4 and sampl\$3 adj (means device circuit)	US-PGPUB; USPAT; USOCR; EPO; JPO; DERWENT	OR	OFF	2005/09/28 11:08
L6	2	5 not 4	US-PGPUB; USPAT; USOCR; EPO; JPO; DERWENT	OR	OFF	2005/09/28 11:11
L7	6030	327/147,151,152,155,156,160,113, 114,115,116,1217,119,120,121,122. ccls.	US-PGPUB; USPAT; USOCR; EPO; JPO; DERWENT	OR	OFF	2005/09/28 11:12
L8	395	7 and divider and frequency adj multipl\$4	US-PGPUB; USPAT; USOCR; EPO; JPO; DERWENT	OR	OFF	2005/09/28 11:13
L9	3841	(divider or dividing) with divisor	US-PGPUB; USPAT; USOCR; EPO; JPO; DERWENT	OR	OFF	2005/09/28 11:14

L10	36	8 and 9	US-PGPUB; USPAT; USOCR; EPO; JPO; DERWENT	OR	OFF	2005/09/28 11:14
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